

IRIX[®] Based Field Diagnostics

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Contributors

Written and revised by Darrin Goss

Edited by Mary Geisert

Illustrated by Darrin Goss

Production by Carlos Miqueo

Engineering contributions by Satish Mirle, Larus Maxwell, Mark Larson, Lisa Steinmetz, Jeff Keopp, and Venkatesh Nayak

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**IRIX® Based Field Diagnostics
Document Number 108-0163-003**

**Silicon Graphics, Inc.
Mountain View, California**

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About This Guide

This document describes the diagnostic tests that you can use while the IRIX operating system is running in a system. These diagnostics run from the IRIX prompt and are called IRIX based field diagnostics.

This document is written for System Support Engineers (SSEs) and Field Engineers (FEs) to use as a reference document in training and on-site.

The information that this document contains is organized into the following chapters:

- Chapter 1, "Introduction to the IRIX Based Field Diagnostics," introduces the IRIX based diagnostics, describes how to install them, and describes how to start the field_diag user interface.
- Chapter 2, "field_diag User Interface," describes the user interface that you can use to run the IRIX based diagnostics.
- Chapter 3, "Diagnostic Tests," describes the individual IRIX based diagnostic tests. It includes instructions on how to run each test from the field_diag user interface and information about the output that each test returns.

This document corresponds to the IRIX based field diagnostics in the *Internal Support Tools 1.2* CD-based diagnostic software release.

Typographical Conventions

This document uses the following typographical conventions:

- Information displayed on the screen is shown in `Courier` type.
- Text that you enter is shown in **Courier bold** type.
- Commands in text, filenames, pathnames, and variables are shown in *italic* type.

Chapter 1

Introduction to the IRIX Based Field Diagnostics

This chapter introduces the IRIX based field diagnostics and provides information about installing them.

1.1 About the IRIX Based Field Diagnostics

The IRIX based field diagnostics are diagnostic tests that you can run from an IRIX prompt while user operations continue on a system. There are two components to the IRIX based field diagnostics:

- The field_diag user interface
- The various diagnostic tests

Note: The System Verification Program (SVP) must be installed to run the field diagnostics because the field diagnostics package uses several of the diagnostics that are in SVP.

1.1.1 About the field_diag Program

You use the field_diag ASCII user interface to run any of the IRIX based field diagnostics that are available for the hardware in your system.

Refer to Chapter 2, “field_diag User Interface,” for more information about the user interface for the field_diag program.

Note: This interface is identical to the user interface for the field stress tool (FST). The similarity of the two interfaces provides a common “look and feel” for performing hardware maintenance.

1.1.2 About the Diagnostic Tests

Diagnostic tests are available to test the following hardware components while IRIX is running on a system:

- CPU hardware
- Ethernet hardware on the BaseIO boards
- Floating-point unit on the Node boards
- Graphics hardware (DIVO and InfiniteReality hardware)
- LINC DMA hardware on the Node boards, ATM boards, and HIPPI-S boards
- MediaIO boards
- Memory
- Networking hardware (ATM board, BaseIO board, HIPPI-S board, MENET board)
- RAID array hardware (restricted to inquiry to the customer-replaceable unit level)
- Router boards, MetaRouter boards, and connecting cables

Refer to Chapter 3, “Diagnostic Tests,” for more information about the diagnostic tests that you can run from the `field_diag` program.

1.2 Installing the IRIX Based Field Diagnostics

The Internal Support Tools group releases the IRIX based field diagnostics on the *Internal Support Tools* CDs. Follow the installation instructions in the CD booklet to install the IRIX based field diagnostics on your system.

Any future updates to the IRIX based field diagnostics will be available from the Internal Support Tools Web site (<http://ist.csd.sgi.com/>) or on future CD releases.

This document corresponds to the IRIX based field diagnostics in the *Internal Support Tools 1.2* CD-based diagnostic software release.

1.3 Summary of Chapter 1

This chapter introduced the IRIX based field diagnostics.

The IRIX based field diagnostics are implemented through two components:

- The `field_diag` user interface that you can use to load, run, and monitor the diagnostic tests
- The individual diagnostic tests

Chapter 2

field_diag User Interface

This chapter describes the field_diag user interface.

2.1 About the field_diag User Interface

The field_diag user interface provides a common ASCII interface that you can use to run the IRIX based field diagnostics. This interface enables you to perform the following actions:

- Select any available diagnostic test
- Modify any user-adjustable parameters for the selected diagnostic test
- Run the selected diagnostic to test the system
- View the output from the selected diagnostic test

This interface is identical to the user interface for the field stress tool (FST). The similarity of the two interfaces provides a common “look and feel” for diagnosing hardware failures.

2.2 Starting the field_diag User Interface

You must have root privilege to run the field_diag user interface.

Perform the following procedure to start the field_diag user interface:

1. Enter the following command to change to the `/usr/diags/diags_interface/` directory:

```
cd /usr/diags/diags_interface
```

2. Enter the following command to start the field_diag program:

```
./field_diag
```


When you type Ctrl+r on the main screen, the Test Log Output window appears. This window displays the output messages from the test that is currently running. Figure 2-2 shows the Test Log Output window.

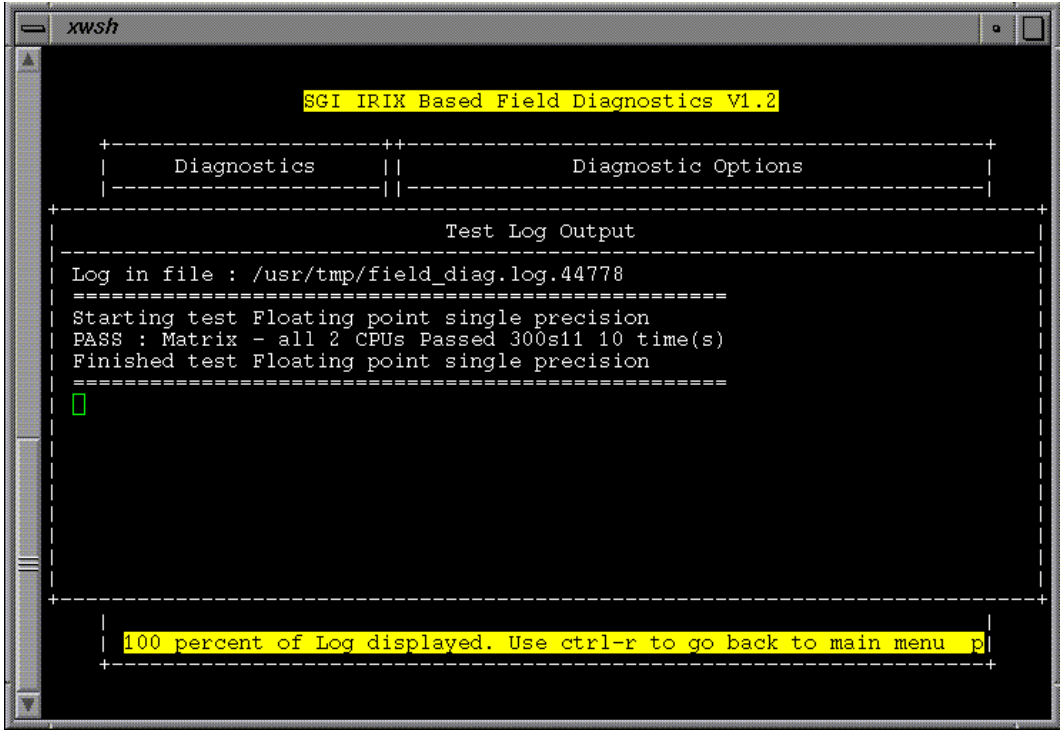


Figure 2-2 field_diag Interface (Test Log Output Window)

2.3.1 Diagnostics List

The Diagnostics list contains the names of all diagnostics that are available for the system configuration that you are using. The field_diag program automatically identifies the hardware in the system and adjusts this list to display only the diagnostics that can test the hardware in the system.

Table 2-1 describes all of the options that are available in the Diagnostics list.

Note: The interface that you use on-site displays only the options that correspond to the hardware in your system.

Table 2-1 Diagnostics List Options

Option	Description
MEMORY	Selects the memory test. Refer to Section 3.2, “Memory Test,” for more information.
CPU-INSTRUCTION	Selects the CPU instruction test. Refer to Section 3.3, “CPU Instruction Test,” for more information.
FPU-SP	Selects the floating-point unit (single-precision) test. Refer to Section 3.4, “Floating-Point Unit (Single-Precision) Test,” for more information.
FPU-DP	Selects the floating-point unit (double-precision) test. Refer to Section 3.5, “Floating-Point Unit (Double-Precision) Test,” for more information.
SCSI	Selects the SCSI thrasher test. Refer to Section 3.6, “SCSI Thrasher Test,” for more information.
NETWORK	Selects the network thrasher test. Refer to Section 3.7, “Network Thrasher Test,” for more information.
ROUTER-CONFIDENCE	Selects the ROUTER-CONFIDENCE test. Refer to Section 3.8, “Router Tests,” for more information.
ROUTER-RANDOM	Selects the ROUTER-RANDOM test. Refer to Section 3.8, “Router Tests,” for more information.
ROUTER-SPECIFIC	Selects the ROUTER-SPECIFIC test. Refer to Section 3.8, “Router Tests,” for more information.
IRSAUDIT	Selects the InfiniteReality graphics test. Refer to Section 3.9, “InfiniteReality Graphics Test,” for more information.
DIVO	Selects the DIVO hardware verification test. Refer to Section 3.10, “DIVO Hardware Verification Test,” for more information.
ATM	Selects the ATM LINC DMA verification test. Refer to Section 3.11, “ATM LINC DMA Verification Test,” for more information.
HIPPI	Selects the HIPPI-S LINC DMA verification test. Refer to Section 3.12, “HIPPI-S LINC DMA Verification Test,” for more information.
MIO	Selects the MediaIO board test. Refer to Section 3.13, “MediaIO Board Test,” for more information.
VHT_LOOPBACK	Selects the vicious HIPPI test (loopback test). Refer to Section 3.14, “Vicious HIPPI Tests,” for more information.

Table 2-1 (continued) Diagnostics List Options

Option	Description
VHT_REMOTE	Selects the vicious HIPPI test (remote test). Refer to Section 3.14, "Vicious HIPPI Tests," for more information.
VST_PING	Selects the vicious socket test (ping test). Refer to Section 3.15, "Vicious Socket Tests," for more information.
VST_RW	Selects the vicious socket test (read and write test). Refer to Section 3.15, "Vicious Socket Tests," for more information.
RAID	Selects the RAID verification test. Refer to Section 3.16, "RAID Verification Test," for more information.
RUNALL	Runs all tests that have default parameter settings. By default, this option skips any tests that have a NEEDED option; however, if you specify values for the NEEDED options for a test before you select this option, this option will also run the test. field_diag stores the results from this option in <i>/usr/tmp/runall_log.pid</i> , where the <i>pid</i> value indicates the process identification value for the process that runs the test.
QUIT	Quits the field_diag program.

Perform any of the following actions to navigate through the Diagnostics list:

- Use the up and down arrow keys to move through the diagnostic tests in the Diagnostics list.
- Use the right arrow key to move from the Diagnostics list into the Diagnostic Options list.
- Use the left arrow key to move from the Diagnostic Options list back into the Diagnostics list.
- Press the Enter key to run the selected test.
- Press the H key to display help messages for the selected test. (Figure 2-3 shows an example of a help message display.)

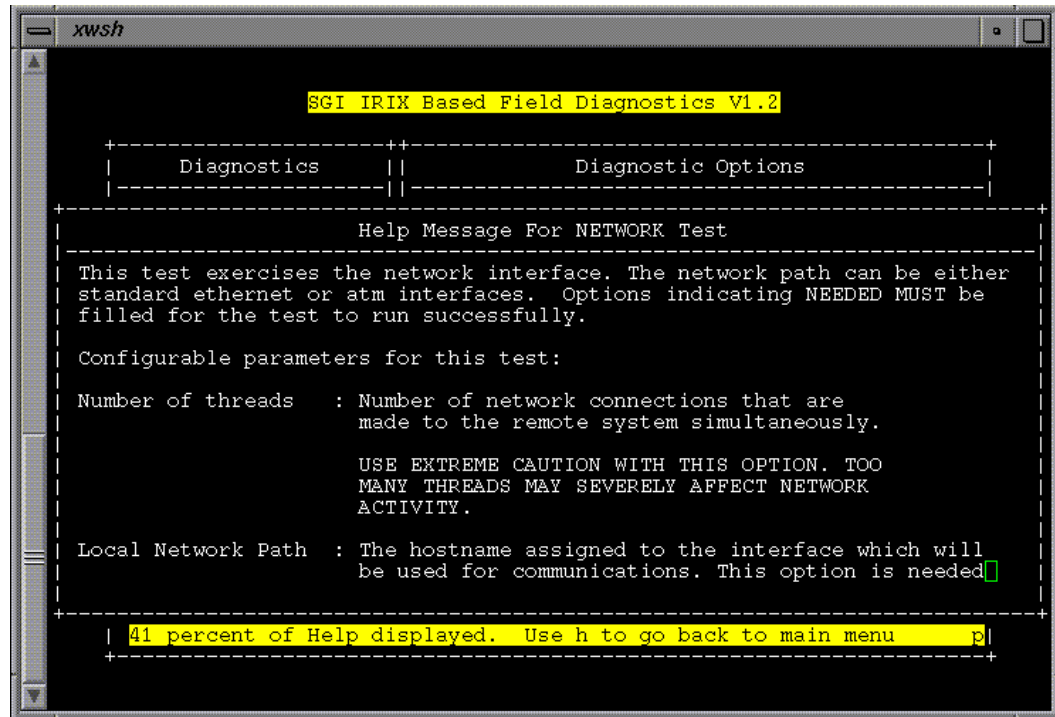


Figure 2-3 Example of Help Message Display

2.3.2 Diagnostic Options List

The Diagnostic Options list shows the parameters that you can adjust for each diagnostic test. If nothing appears in this list, the test does not have user-adjustable parameters.

For example, Figure 2-4 shows that the SCSI thrasher test has two parameters that you can adjust: the number of writes that the test performs and the size of the file that the test writes.

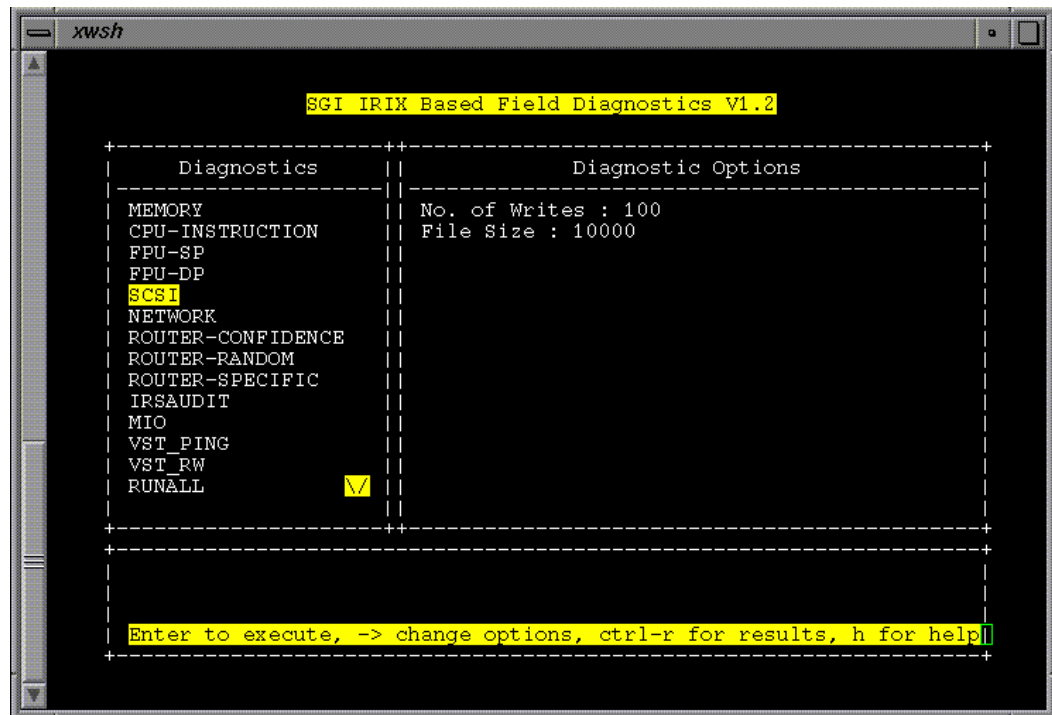


Figure 2-4 Example of Diagnostic Options List

Perform any of the following actions to navigate through the Diagnostic Options list:

- Use the up and down arrow keys to move through the parameters in the Diagnostic Options list.
- Press the Enter key to select the parameter that you want to change. (When the prompt Enter New Value for the Option appears in the diagnostic status area, type the new value and press the Enter key.)
- Use the left arrow key to move from the Diagnostic Options list into the Diagnostics list.

2.3.3 Diagnostic Status Area

The diagnostic status area provides information about the current status of the interface and diagnostic test that is running. It displays the following information:

- A prompt that indicates what you can do on the current screen (for example, Hit enter to execute, Right Arrow to change options)
- A status line that indicates what action the field_diag program is currently performing (for example, Loading Diags ... and Executing : memory)
- Status information about the state of the selected test (for example, memory Test Passed)

2.3.4 Test Log Output Window

The Test Log Output window displays output from the test that is currently running. This information includes any error messages that the test generates. If the output contains more than one screen of text, use the up and down arrow keys to scroll through the text. (The diagnostic status area on the main screen displays the percentage of the output that you have viewed.)

Enter Ctrl+r to toggle the display of this window to the main window. Refer again to Figure 2-2 for an example of a Test Log Output window.

2.4 Summary of Chapter 2

This chapter described the field_diag user interface.

The field_diag user interface provides a common ASCII interface from which you can run and monitor all IRIX based field diagnostics that are available for the hardware in your system. The interface has four main components:

- The Diagnostics list on the main screen
- The Diagnostic Options list on the main screen
- The diagnostic status area on the main screen
- The Test Log Output window

You can use the interface to perform the following actions:

- To select any available diagnostic test
- To modify any user-adjustable parameters for the selected diagnostic test
- To run the selected diagnostic to test the system
- To view the output from the diagnostic test

Table 2-2 summarizes the keyboard characters that you can use to navigate through the interface.

Table 2-2 field_diag Keyboard Commands

Interface Location	Keyboard Character	Navigation
Diagnostics list	Up arrow	Move up to the previous diagnostic test in the list
	Down arrow	Move down to the next diagnostic test in the list
	Right arrow	Move into the Diagnostic Options list
	Enter	Execute the selected diagnostic test
	H	Display the help information for the current test
Diagnostic Options list	Up arrow	Move up to the previous option in the list
	Down arrow	Move down to the next option in the list
	Left arrow	Move into the Diagnostics list
	Enter	Select the parameter to modify
Anywhere	Ctrl+r	Toggle the display of the Test Log Output window

Chapter 3

Diagnostic Tests

This chapter describes the following diagnostic tests, which you can run from the field_diag user interface:

- Memory test
- CPU instruction test
- Floating-point unit (single-precision) test
- Floating-point unit (double-precision) test
- SCSI thrasher test
- Network thrasher test
- Router tests
- InfiniteReality graphics test
- DIVO hardware verification test
- ATM LINC DMA verification test
- HIPPI-S LINC DMA verification test
- MediaIO board test
- Vicious socket tests
- Vicious HIPPI tests
- RAID verification test

Note: This document describes the tests in the order that they appear in the interface.

3.1 Test Availability

Table 3-1 indicates the diagnostic tests that are available for each operating system that the IST 1.2 CD-based diagnostic release supports.

Table 3-1 Diagnostic Test Availability (by Operating System)

Diagnostic Test	Operating System ^a		
	IRIX 6.5	IRIX 6.4	IRIX 6.2
Memory test	x	x	x
CPU instruction test	x	x	
Floating-point unit (single-precision and double-precision) tests	x	x	x
SCSI thrasher test	x	x	x
Network thrasher test	x	x	x
Router tests	x		
InfiniteReality graphics test	x	x	x
DIVO hardware verification test	x	x	
ATM LINC DMA verification test	x	x	x
HIPPI-S LINC DMA verification test	x	x	x
MediaIO board test	x	x	x
Vicious socket tests	x	x	x
Vicious HIPPI tests	x	x	x
RAID verification test	x	x	

a. The IRIX based field diagnostics are not available for IRIX versions 6.3 or 5.3.

Table 3-2 indicates the diagnostic tests that are available for each type of IP processor board that the IST CD1.2 CD-based diagnostic release supports.

Table 3-2 Diagnostic Test Availability (by IP Type)

Diagnostic Test	IP Type ^a											
	IP17	IP19	IP20	IP21	IP22	IP25	IP26	IP27	IP28	IP29	IP30	IP31
Memory test		x		x		x		x		x	x	x
CPU instruction test								x				x
Floating-point unit (single-precision and double-precision) tests	x	x	x	x	x	x	x	x	x	x	x	x
SCSI thrasher test	x	x	x	x	x	x	x	x	x	x	x	x
Network thrasher test	x	x	x	x	x	x	x	x	x	x	x	x
Router tests								x		x		x
InfiniteReality graphics test				x		x		x				x
DIVO hardware verification test								x				x
ATM LINC DMA verification test								x				x
HIPPI-S LINC DMA verification test								x				x
Vicious socket tests	x	x	x	x	x	x	x	x	x	x	x	x
MediaIO board test								x				x
Vicious HIPPI tests		x		x		x		x				x
RAID verification test								x		x		x

a. The IRIX based field diagnostics are not available for IP32 processors.

3.2 Memory Test

The memory test checks the memory subsystem by performing sparse writes of data patterns, reading the data back, and verifying the results.

The amount of memory that it tests depends on the limit size that you specify with the `Memory size` parameter. If you set the `Memory size` parameter to `unlimited`, this test attempts to check the maximum memory size of the system. (This test attempts to use 85 percent of the memory that you specify.)

Caution: If you set the `Memory size` parameter to `unlimited` and the system contains a large amount of memory, this test will run for an undetermined length of time.

If this test fails, the failing FRU is a DIMM on the Node board that the test specifies, the Node board that the test specifies, or the midplane.

3.2.1 Prerequisites for Running the Memory Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must run this test from superuser mode.

3.2.2 Running the Memory Test

To run the memory test from the `field_diag` program, perform the following procedure:

1. Select the `MEMORY` option from the Diagnostics list.
2. If you want to modify the parameter `Memory Size (kbytes)` (which specifies the amount of memory to test), perform the following actions; otherwise, skip to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list to select the `Memory Size (kbytes)` parameter.
 - Press the `Enter` key.
 - At the prompt `Enter New Value for the Option in the diagnostic status area`, enter the new value.
 - Press the `Enter` key.

3. Press the left arrow key to return to the MEMORY option in the Diagnostics list.
4. Press the **Enter** key to start running the memory test.
The message `Executing : memory` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the memory test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.2.3 Output from the Memory Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.2.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `memory Test Passed`.

3.2.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `memory Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.3 CPU Instruction Test

The CPU Instruction test verifies all integer operations, floating-point operations, branches, and memory load and store operations. It uses all of the available virtual address space to test the address translation logic of the processor that is tested.

Note: This test supports only R8000 and R10000 processors running IRIX 6.4 or IRIX 6.5. It cannot run with any other operating system or processors.

If this test fails, the failing FRU is a Node board or the midplane.

3.3.1 Prerequisites for Running the CPU Instruction Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX 6.5 operating system booted and running.
- The system that you want to test must include only R8000 and/or R10000 processors.
- The system must be dedicated for testing; that is, the system that you want to test cannot be running any other user jobs.

3.3.2 Running the CPU Instruction Test

To run the CPU Instruction test from the `field_diag` program, perform the following procedure:

1. Select the CPU-INSTRUCTION option from the Diagnostics list.
2. Press the **Enter** key to start running the test.

The message `Executing : CPU-INSTRUCTION Test` appears in the diagnostic status area, which indicates that the test is running.

3. View the diagnostic status area and Test Log Output window for messages from the processor test.

Note: Refer to the `olperi` reference page for information about running this test from the command line.

3.3.3 Output from the CPU Instruction Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.3.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `Processor Test Passed`.

3.3.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `Processor Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure. A typical failure message uses the following format:

```
Cpu31 failed at pass 897
```

This message reports that CPU 31 failed. Enter the `hinv -v` command at an IRIX prompt to determine the physical location of a CPU.

3.4 Floating-Point Unit (Single-Precision) Test

The floating-point unit (single-precision) test uses matrix manipulations of single-precision numbers to test the floating-point unit.

If this test fails, the failing FRU is the Node board that contains the floating-point unit that is tested or the midplane.

3.4.1 Prerequisites for Running the Floating-Point Unit (Single-Precision) Test

This test has the following prerequisites:

- The system that you are testing must have the IRIX operating system booted and running.
- The FORTRAN libraries must be located in `/usr/lib/libftn.so`.

3.4.2 Running the Floating-Point Unit (Single-Precision) Test

To run the floating-point unit (single-precision) test from the `field_diag` program, perform the following procedure:

1. Select the FPU-SP option from the Diagnostics list.
2. If you want to modify the parameter `Number of Iterations` (which specifies the number of matrix manipulations that the test should perform), perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list to select the `Number of Iterations` parameter.
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.
3. Press the left arrow key to return to the FPU-SP option in the Diagnostics list.
4. Press the **Enter** key to start running the floating-point unit (single-precision) test.

The message `Executing : Floating point single precision` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the floating-point unit (single-precision) test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.4.3 Output from the Floating-Point Unit (Single-Precision) Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.4.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `Floating point single precision Test Passed`.

3.4.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `Floating point single precision Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.5 Floating-Point Unit (Double-Precision) Test

The floating-point unit (double-precision) test uses matrix manipulations of double-precision numbers to test the floating-point unit.

If this test fails, the failing FRU is the Node board that contains the floating-point unit that is being tested or the midplane.

3.5.1 Prerequisites for Running the Floating-Point Unit (Double-Precision) Test

This test has the following prerequisites:

- The system that you are testing must have the IRIX operating system booted and running.
- The FORTRAN libraries must be located in `/usr/lib/libftn.so`.

3.5.2 Running the Floating-Point Unit (Double-Precision) Test

To run the floating-point unit (double-precision) test from the `field_diag` program, perform the following procedure:

1. Select the FPU-DP option from the Diagnostics list.
2. If you want to modify the parameter `Number of Iterations` (which specifies the number of matrix manipulations that the test should perform), perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list to select the parameter `Number of Iterations`.
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.
3. Press the left arrow key to return to the FPU-DP option in the Diagnostics list.
4. Press the **Enter** key to start running the floating-point unit (double-precision) test.

The message `Executing : Floating point double precision` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the floating-point unit (double-precision) test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.5.3 Output from the Floating-Point Unit (Double-Precision) Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.5.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `Floating point double precision Test Passed`.

3.5.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `Floating point double precision Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.6 SCSI Thrasher Test

The SCSI thrasher test stresses the SCSI subsystem. This diagnostic tests the SCSI hardware on MSCSI and BaseIO boards.

The test creates multiple processes that perform synchronized writes and reads. Each process writes a data pattern to a SCSI drive and then waits for the other processes to finish writing data patterns. When all processes have written their data patterns, the test reads the data back and compares that data with the data that was written. This test uses five different data patterns.

This test uses the following algorithm:

1. The tests mounts any SCSI drives and starts a semaphore program. (The semaphore program synchronizes all disk processes to ensure that no read operations are performed before all processes finish writing data.)
2. The test creates a */tmp* directory on the system that is tested if the directory does not already exist.
3. The test starts two copies of a *diskthrash* process for each mounted disk.
4. Each of the *diskthrash* processes writes data to two files in the */tmp* directory. The data files contain random numbers.
5. All processes wait for the semaphore program to signal that all processes have finished writing data to the */tmp* directory.
6. All processes read data back from the SCSI drives at the same time, which stresses the I/O channels with maximum traffic.
7. The test compares the data that was read back with the data that was written.
8. The test deletes all temporary files it created, deletes all temporary directories that it created, and unmounts any SCSI drives that it mounted.
9. The test generates a pass or failure message and stops.

Note: The *diskthrash* processes exit if they detect more than ten errors during the write process or read process.

If this test fails, the failing FRU is a BaseIO board, midplane, MSCSI board, or disk drive.

3.6.1 Prerequisites for Running the SCSI Thrasher Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must have disk space available on the system that you are testing to create the temporary files that this test uses.

3.6.2 Running the SCSI Thrasher Test

Caution: Run only one copy of this test at a time. If you run more than one copy at a time, the system may hang.

1. Select the SCSI option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-3 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-3 SCSI Thrasher Test User-Adjustable Parameters

Parameter	Description
No. of Writes	Specifies the number of writes that the test will perform.
File Size	Specifies the size of the files that the test will generate.

3. Press the left arrow key to return to the SCSI option in the Diagnostics list.
4. Press the **Enter** key to start running the SCSI thrasher test.

The message `Executing : randomthrash` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the SCSI thrasher test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.6.3 Output from the SCSI Thrasher Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.6.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `randomthrash Passed`.

3.6.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `randomthrash Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure. Possible messages include:

- `ERROR : Diskthrasher Thrasher Failed. Check error.log for error`
The test failed. The *error.log* file contains more information about the failure. The failing FRU is the BaseIO board, MSCSI board, or disk drive.
- `ERROR : Data ERROR on device device_name`
`Data mismatched at disk offset value Expected data expected Act. actual`
The test failed. The data that was read back did not equal the data that was written. The failing FRU is the BaseIO board, MSCSI board, or disk drive.

3.7 Network Thrasher Test

The network thrasher test stresses the specified network hardware by sending large blocks of data to a remote system and then reading the data back.

The test uses the following client/server model:

- Multiple clients on the system that you are testing send data to a server on a remote system through a network connection.
- The remote server returns the data to a server on the system that you are testing.

The test compares the data that was returned with the data that was sent. (Figure 3-1 shows an example of this process with three clients.)

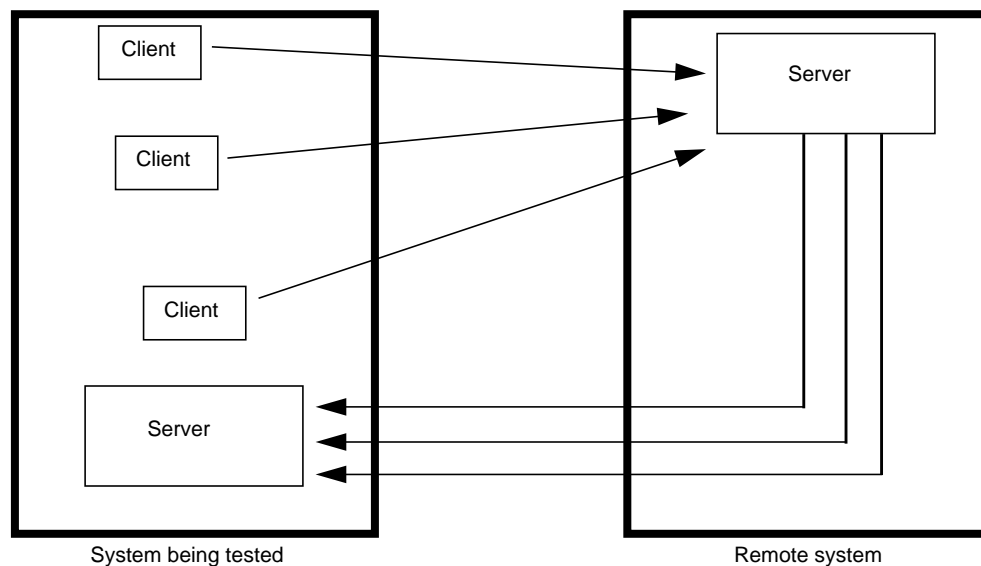


Figure 3-1 Network Thrasher Test Flow

The client processes run on the system that you are testing and simultaneously perform the following actions:

- Each client sends the address to which the server on the remote system should return the data.
- Each client transfers a large file (*/unix*) to the server on the remote system.

One server process runs on the remote system and forks a process for each connection that it receives from a client. Each forked process performs the following actions:

- The process reads the data that was sent from the client on the system that is tested.
- The process returns the data to a server process on the system that is tested.

The server process on the system that you are testing receives the data from the server on the remote system. The test compares the data that was received with the data that was sent.

If this test fails, the failing FRU is the BaseIO board or midplane.

3.7.1 Prerequisites for Running the Network Thrasher Test

This test has the following prerequisites:

- The system that you are testing must have the IRIX operating system booted and running.
- You must have access to a remote system that is functional and running the IRIX operating system.
- You must specify values for all parameters in the Test Options list that are set to `NEEDED` by default.
- You must have disk space available on the system that you are testing to create the temporary files that this test uses. The amount of disk space that this test needs depends on the number of clients that you want it to fork.
- You must set the `remote host` parameter correctly for the system that you are testing.
- The `/etc/hosts` file must have the proper information (IP address with fully qualified hostname) for the system that you want to test. For example:

```
150.166.14.31 bootleg.csd.sgi.com bootleg
```

3.7.2 Running the Network Thrasher Test

To run the network thrasher test from the `field_diag` program, perform the following procedure:

1. Select the `NETWORK` option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-4 describes the parameters that you can modify.)
 - Press the `Enter` key.
 - At the prompt `Enter New Value for the Option in the diagnostic status area`, enter the new value.
 - Press the `Enter` key.

Table 3-4 Network Thrasher Test User-Adjustable Parameters

Parameter	Description
Number of Threads	Specifies the number of network connections that the test will use. This test causes extreme stress on the network. Use caution when you adjust the number of threads that this test will use. Setting this parameter too high will adversely affect network performance and the performance of other systems on the network.
Local Network Path	Specifies the hostname that is assigned to the interface to be used for communications. (This option enables you to test nondefault interfaces.) Enter a hostname; for example, bootleg.sgi.com or atm-bootleg.sgi.com. You must set this parameter.
Remote Host Name	Specifies the remote host with which the local host will communicate. (Enter a hostname or IP address; for example, oddity.sgi.com or 150.166.237.10.) You must set this parameter.
Login Name	Specifies the username that the test will use to log into the remote system.
Password	Specifies the password that the test will use to access the remote system. If the username that you want to use does not require a password (for example, the guest account), leave this parameter blank.

3. Press the left arrow key to return to the NETWORK option in the Diagnostics list.
4. Press the **Enter** key to start running the network thrasher test.
The message `Executing : network` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the network thrasher test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.7.3 Output from the Network Thrasher Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.7.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `network Test Passed`.

3.7.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes it unable to continue testing, the diagnostic status area of the user interface displays the message `network Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure. Possible messages include:

- `ERROR: Network Thrasher Failed. Check error log for error`

The test failed with an error. This error occurs when a data integrity error occurs after the data transfer is completed. The failing FRU is the board that contains the network interface that is tested.

- `ERROR: Remote System is not responding. Cannot Continue`

The test failed with an error. This error occurs when the test checks to determine whether the remote system is accessible. The remote system may not be responding for many reasons. (For example, the network is not functioning, the remote system is down, or the test system is not configured correctly.)

- `ERROR: Could not fork off requested number of clients`

The test failed with an error. This error occurs if the number of clients is greater than the number of processes that can be forked.

3.8 Router Tests

The router tests are directed tests that focus on specific components of the internode communication hardware. These tests check the following logic: network data paths, network addressing, and network cache coherency.

Three versions of the router tests are available:

- The ROUTER-CONFIDENCE test accesses all paths in the system across all nodes. This test uses all system router paths (Routers and MetaRouters) that the system topology defines. This test performs data comparisons to detect any errors.
- The ROUTER-RANDOM test transfers large amounts of random data to and from random addresses that are distributed across nodes. This test uses all system router paths (Routers and MetaRouters) that the system topology defines. This test performs data comparisons to detect any errors.
- The ROUTER-SPECIFIC test transfers large amounts of random data to and from random addresses on two nodes that you specify. This test does not perform data comparisons; instead, it relies on the hardware to detect any errors.

If any of these tests fails, the failing FRU is a Node board, Router board, MetaRouter board, CrayLink cable, or midplane.

3.8.1 Prerequisites for Running the Router Tests

The router tests have the following prerequisites:

- The system that you want to test must have the IRIX 6.4 (or later) operating system booted and running.
- The system that you want to test must include Router boards. (This test is available only for Origin200, Origin2000, Onxy2, and CRAY Origin2000 systems.)
- No other applications should be running in the system when you use the router tests.

3.8.2 Running the Router Tests

You can run the router tests from the `field_diag` program.

3.8.2.1 Running the ROUTER-CONFIDENCE Test

To run the ROUTER-CONFIDENCE test from the `field_diag` program, perform the following procedure:

1. Select the ROUTER-CONFIDENCE option from the Diagnostics list.

2. If you want to modify the parameter `Number of iterations` (which specifies the number of times that the test should run), perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list to select the parameter `Number of iterations`.
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.
3. Press the left arrow key to return to the `ROUTER-CONFIDENCE` option in the Diagnostics list.
4. Press the **Enter** key to start running the `ROUTER-CONFIDENCE` test.
The message `Executing : ROUTER-CONFIDENCE` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the `ROUTER-CONFIDENCE` test.

3.8.2.2 Running the `ROUTER-RANDOM` Test

To run the `ROUTER-RANDOM` test from the `field_diag` program, perform the following procedure:

1. Select the `ROUTER-RANDOM` option from the Diagnostics list.
2. If you want to modify the parameter `Number of iterations` (which specifies the number of times that the test should run), perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list to select the parameter `Number of iterations`.
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.
3. Press the left arrow key to return to the `ROUTER-RANDOM` option in the Diagnostics list.
4. Press the **Enter** key to start running the `ROUTER-RANDOM` test.
The message `Executing : ROUTER-RANDOM` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the `ROUTER-RANDOM` test.

3.8.2.3 Running the ROUTER-SPECIFIC Test

To run the ROUTER-SPECIFIC test from the field_diag program, perform the following procedure:

1. Select the ROUTER-SPECIFIC option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-5 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-5 ROUTER-SPECIFIC Test User-Adjustable Parameters

Parameter	Description
Source Module	Specifies the module number from which the test will transfer data. (For example, enter a 1 for module 1.) You must enter this parameter.
Source Node	Specifies the node number within the source module from which the test will transfer data. (For example, enter a 1 for node n1.) You must enter this parameter. (Use the <code>icrash -S -e config</code> command to view a list of available nodes.)
Target Module	Specifies the module number to which the test will transfer data. (Example, enter a 2 for module 2.) You must enter this parameter.
Target Node	Specifies the node number within the target module to which the test will transfer data. (For example, enter a 3 for node n3.) You must enter this parameter. (Use the <code>icrash -S -e config</code> command to view a list of available nodes.)
Number of iterations	Specifies the number of times that the test will run.

3. Press the left arrow key to return to the ROUTER-SPECIFIC option in the Diagnostics list.
4. Press the **Enter** key to start running the ROUTER-SPECIFIC test.
The message `Executing : ROUTER-SPECIFIC` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the ROUTER-SPECIFIC test.

3.8.3 Output from the Router Tests

The router tests return output to the diagnostic status area of the user interface and the Test Log Output window.

3.8.3.1 Pass Output

The router tests display the following pass output:

- If the ROUTER-CONFIDENCE test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `ROUTER-CONFIDENCE Test Passed.`
- If the ROUTER-RANDOM test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `ROUTER-RANDOM Test Passed.`
- If the ROUTER-SPECIFIC test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `ROUTER-SPECIFIC Test Passed.`

3.8.3.2 Failure Output

The router tests display the following failure output:

- If the ROUTER-CONFIDENCE test detects a hardware failure or encounters an error that makes it unable to continue, the diagnostic status area of the user interface displays the message `ROUTER-CONFIDENCE Test Failed.`
- If the ROUTER-RANDOM test detects a hardware failure or encounters an error that makes it unable to continue, the diagnostic status area of the user interface displays the message `ROUTER-RANDOM Test Failed.`
- If the ROUTER-SPECIFIC test detects a hardware failure or encounters an error that makes it unable to continue, the diagnostic status area of the user interface displays the message `ROUTER-SPECIFIC Test Failed.`

When one of these messages appears, view the Test Log Output window for more information about the cause of the failure. This information uses the format shown in the following example output:

```
tid4 Error Report: test section 3, pass = 12
source node = /hw/module/1/slot/n1/node
destination node = /hw/module/2/slot/n2/node
failing address = 0x1708a880
expected data = 0xa98f7d4f12b04567
actual data = 0xcc06a8394f9ab003
```

If any of these tests fails, the failing FRU is a Node board, Router board, MetaRouter board, or connecting cable.

The following information is useful for troubleshooting errors that the router tests find:

- If the router tests detect an error, they report the physical source and destination nodes and then exit. Because the fault can occur anywhere between the source and destination nodes, you should rerun the router tests until a pattern emerges that indicates which node is actually failing.

Example 1: If the tests fail with three different failures but the same node is always listed as a source or destination node, then that node is most likely causing the failure.

Example 2: If there is a common node that is on the path between the source and destination nodes for multiple failures, then the common node is most likely causing the failure.

- If both ends of a failing link are on the same board, that board is the failing FRU; otherwise, the failing link spans more than one board, and you will need to swap boards and/or cables to determine which part of the link is failing.
- The ROUTER-CONFIDENCE test performs data comparisons and may detect stale or corrupt data that the hardware error detection misses.
- You can use the ROUTER-SPECIFIC test to exercise a single path after finding a failure with the ROUTER-CONFIDENCE or ROUTER-RANDOM test; however, the ROUTER-SPECIFIC test may not always reproduce the failures that were encountered by the ROUTER-CONFIDENCE and ROUTER-RANDOM tests.
- The ROUTER-SPECIFIC test does not perform data comparisons, so it relies on the hardware error detection to show any errors. You can use the *linkstat* command to view these errors.
- If the hardware detects a fatal fault, the router tests abort. When this happens, the system console displays the error and its location.
- If the hardware detects a recoverable error, use the *linkstat* command to view it.
- If the hardware detects an error, the error counters on the Hubs and Routers should advance. Use the *linkstat* command to view errors that the hardware detects.
- The system console should report any fatal errors.
- View *syslog* for error information.

3.9 InfiniteReality Graphics Test

The InfiniteReality graphics test (irsaudit) checks the InfiniteReality graphics hardware.

If this test fails, the failing FRU is one of the graphics subsystems (RM, GE, or DG) or midplane. The test output indicates which subsystem is failing.

3.9.1 Prerequisites for Running the InfiniteReality Graphics Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must run this test from superuser mode.
- You must run this test from an alternate terminal; you cannot run this test from the graphics console.

3.9.2 Running the InfiniteReality Graphics Test

To run the InfiniteReality graphics test from the field_diag program, perform the following procedure:

1. Select the IRSAUDIT option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-6 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option in the diagnostic status area`, enter the new value.
 - Press the **Enter** key.

Table 3-6 InfiniteReality Graphics Test User-Adjustable Parameters

Parameter	Description
Output TRCE Messages	Specifies whether the test should output TRCE messages. Set this parameter to <code>-notrace</code> (to specify that the test not print these messages) or <code>-trace</code> (to specify that the test print these messages).
Output CODE Messages	Specifies whether the test should output CODE messages. Refer to Table 3-7 for more information about CODE messages. Set this parameter to <code>-nocode</code> (to specify that the test not print these messages) or <code>-code</code> (to specify that the test print these messages).
Output INFO Messages	Specifies whether the test should output INFO messages. Refer to Table 3-7 for more information about INFO messages. Set this parameter to <code>-noinfo</code> (to specify that this test not print these messages) or <code>-info</code> (to specify that the test print these messages).
Continue on Error	Specifies whether or not the test will continue when it detects an error. Set this parameter to <code>-continue</code> to continue testing when the test detects an error.

3. Press the left arrow key to return to the IRSAUDIT option in the Diagnostics list.
4. Press the **Enter** key to start running the InfiniteReality graphics test.
The message `Executing : IRSAUDIT` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the InfiniteReality graphics test.

Note: Refer to the `irsaudit` reference page for information about running this test from the command line.

3.9.3 Output from the InfiniteReality Graphics Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window. The output includes pass messages, failure messages, and messages that are tagged with the four-character identifiers shown in Table 3-7.

Table 3-7 InfiniteReality Graphics Test Output Tags

Tag	Description
TEST	Test start message. This message, which is generated at the beginning of the test, includes the symbolic name of the test and a description of the test.
RSLT	Test result message. This message, which is generated at the end of a test, includes the symbolic name of the test and the test result. The result is either pass, fail, unresolved, or untested.
DIAG	Diagnosis message. This message precedes any “fail” or “unresolved” message. The message indicates the components or wires that are possible causes of the failure.
INFO	Informational message. This message provides test progress reports, expected/received pairs, or other useful information. This message is useful to an advanced user of the test.
DEBUG	Debugging informational message. This message is not intended for normal field use of this test but can be useful to diagnostic programmers. The test generates this type of message only when debugging output is turned on (not the default).
TIME	Time stamp. This message provides information at important time boundaries, such as the beginning and end of the test.
META	Summary message from multiple test runs. This message summarizes information from multiple test runs. The test generates a table that provides the pass and fail counts of each test run (and totals for all test runs) across multiple full-test loops.
ABRT	Abort message. This message reports an exceptional error condition that causes the test to abort. (Possible causes include a malloc failure, unexpected system call failure, or assertion failure.) This message should not appear under normal circumstances.
CODE	Encoded message. This message encodes board and ASIC failures for easy parsing.

Refer to the [irsaudit](#) online reference page for more information about the test output tags.

3.9.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `IRSAUDIT Test Passed`.

3.9.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `IRSAUDIT Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.10 DIVO Hardware Verification Test

The DIVO hardware verification test (divotest) checks the Digital Video (DIVO) hardware in a system.

If this test fails, the failing FRU is a DIVO board or midplane.

3.10.1 Prerequisites for Running the DIVO Hardware Verification Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must run this test from superuser mode.

3.10.2 Running the DIVO Hardware Verification Test

To run the DIVO hardware verification test from the field_diag program, perform the following procedure:

1. Select the DIVO option from the Diagnostics list.
2. Press the **Enter** key to start running the DIVO video hardware verification test.

The message `Executing : DIVO` appears in the diagnostic status area, which indicates that the test is running.

3. View the diagnostic status area and Test Log Output window for messages from the DIVO hardware verification test.

Note: Refer to the divotest reference page for information about running this test from the command line.

3.10.3 Output from the DIVO Hardware Verification Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window. The output includes pass messages, failure messages, and messages that are tagged with the four-character identifiers shown in Table 3-8.

Table 3-8 DIVO Hardware Verification Test Output Tags

Tag	Description
TEST	Test start message. This message, which is generated at the beginning of the test, includes the symbolic name of the test and a description of the test.
RSLT	Test result message. This message, which is generated at the end of a test, includes the symbolic name of the test and the test result. The result is either pass, fail, unresolved, or untested.
DIAG	Diagnosis message. This message precedes any “fail” or “unresolved” message. This message indicates the components or wires that are possible causes of the failure.
INFO	Informational message. This message provides test progress reports, expected/received pairs, or other useful information. This message is useful to an advanced user of the test.
DEBUG	Debugging informational message. This message is not intended for normal field use of this test but can be useful to diagnostic programmers. The test generates this type of message only when debugging output is turned on (not the default).
TIME	Time stamp. This message provides information at important time boundaries, such as the beginning and end of the test.
META	Summary message from multiple test runs. This message summarizes information from multiple test runs. The test generates a table that provides the pass and fail counts of each test run (and totals for all test runs) across multiple full-test loops.
ABRT	Abort message. This message reports an exceptional error condition that causes the test to abort. (Possible causes include a malloc failure, unexpected system call failure, or assertion failure.) This message should not appear under normal circumstances.
CODE	Encoded message. This message encodes board and ASIC failures for easy parsing.

Refer to the divotest online reference page for more information about the test output tags.

3.10.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `DIVO Test Passed`.

3.10.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes it unable to continue testing, the diagnostic status area of the user interface displays the message `DIVO Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.11 ATM LINC DMA Verification Test

The ATM LINC DMA verification test verifies that DMA operations between the CPUs and ATM boards are successful. This test uses the following algorithm:

1. The test performs DMA transfers of 8-KB data blocks from the host to LINC0 on the specified ATM network card.
2. The test uses a DMA operation to read the data back into a buffer in memory.
3. The test uses programmed I/O (PIO) operations to access the data in memory and verifies that the data that is read back with a DMA operation is the same as the data that was written with a DMA operation.
4. The test repeats Steps 1 through 3 using LINC1 on the network card instead of LINC0.

If this test fails, the failing field-replaceable unit (FRU) is a Node board, ATM board, or midplane.

3.11.1 Prerequisites for Running the ATM LINC DMA Verification Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must run this test from superuser mode.

3.11.2 Running the ATM LINC DMA Verification Test

To run the ATM LINC DMA verification test from the `field_diag` program, perform the following procedure:

1. Select the ATM option from the Diagnostics list.
2. Press the **Enter** key to start running the ATM LINC DMA verification test.

The message `Executing : ATM` appears in the diagnostic status area, indicating that the test is running.

3. View the diagnostic status area and Test Log Output window for messages from the ATM LINC DMA verification test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.11.3 Output from the ATM LINC DMA Verification Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.11.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `ATM TEST Passed`.

3.11.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue testing, the diagnostic status area of the user interface displays the message `ATM Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.12 HIPPI-S LINC DMA Verification Test

The HIPPI-S LINC DMA verification test verifies that DMA operations between the CPUs and HIPPI-S boards are successful. This test uses the following algorithm:

1. The test performs DMA transfers of 8-KB data blocks from the host to LINC0 on the specified HIPPI-S network card.
2. The test uses a DMA operation to read the data back into a buffer in memory.
3. The test uses programmed I/O (PIO) operations to access the data in memory and verifies that the data that is read back with a DMA operation is the same as the data that was written with a DMA operation.
4. The test repeats Steps 1 through 3 using LINC1 on the network card instead of LINC0.

If this test fails, the failing FRU is a Node board, HIPPI-S board, or midplane.

3.12.1 Prerequisites for Running the HIPPI-S LINC DMA Verification Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must run this test from superuser mode.

3.12.2 Running the HIPPI-S LINC DMA Verification Test

To run the HIPPI-S LINC DMA verification test from the `field_diag` program, perform the following procedure:

1. Select the HIPPI option from the Diagnostics list.
2. Press the **Enter** key to start running the LINC DMA verification test.
The message `Executing : HIPPI` appears in the diagnostic status area, which indicates that the test is running.
3. View the diagnostic status area and Test Log Output window for messages from the HIPPI-S LINC DMA verification test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.12.3 Output from the HIPPI-S LINC DMA Verification Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.12.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `HIPPI TEST Passed`.

3.12.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `HIPPI Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.13 MediaIO Board Test

The MediaIO (MIO) board test checks any MIO boards in the system. It tests the keyboard and mouse ports, audio hardware (headphone port, microphone point, line-out jack, line-in jack, ADAT connector, AES connector, RAD chip, parallel port, serial port, and UST clock signal), and AES Out signal.

If this test fails, the failing FRU is the MediaIO board or midplane.

3.13.1 Prerequisites for Running the MediaIO Board Test

This test has the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must connect a printer to the parallel port to run this test. If you do not connect a printer, the test fails.
- You must connect a keyboard and mouse to the keyboard/mouse port to run this test. If you do not connect a keyboard and mouse, the test fails.

3.13.2 Running the MediaIO Board Test

To run the MediaIO board test from the `field_diag` program, perform the following procedure:

1. Select the MIO option from the Diagnostics list.
2. Press the **Enter** key to start running the MediaIO board test.
The message `Executing : MIO Test` appears in the diagnostic status area, which indicates that the test is running.
3. View the diagnostic status area and Test Log Output window for messages from the MediaIO board test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.13.3 Output from the MedialO Board Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.13.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `MIO Test PASSED`.

3.13.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `MIO Test Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure. Possible messages include:

- `Digital Test on RAD1.AESOut -> RAD1.AESIn: FAIL`
The test failed. The failing FRU is the MIO board that is tested.
- `rad_diag Status: FAIL`
The test failed. The failing FRU is the MIO board that is tested.
- `rad_diag: RSLT:FAIL bad return code`
`comparison failed`
`loopback AES failed`
`comparison failed`
`loopback AES failed`
`comparison failed`
`loopback AES failed`
`comparison failed`
`loopback AES failed`
The test failed. The failing FRU is the MIO board that is tested.

Note: If this test fails with a UST clock failure, the failing FRU may be the midplane.

3.14 Vicious HIPPI Tests

The vicious HIPPI tests are exercisers that test HIPPI channel pairs. There are two vicious HIPPI tests:

- The VHT_LOOPBACK test uses raw channel HIPPI data to exercise the configured HIPPI network device on the system that you are testing. It sends and receives data through a loopback cable on the HIPPI-S board. It then compares the actual data with expected values.
- The VHT_REMOTE test uses raw channel HIPPI data to exercise the configured HIPPI network device on the system that you are testing. It can run on one system with a loopback cable installed or between two connected systems.

To exercise the HIPPI interface on one system, it sends and receives data through the loopback cable on the HIPPI-S board. It then compares the actual data with expected values. To run the test on one system, set the `Local Interface` and `Remote Interface` parameters to the same value. (Refer to Table 3-10.)

To exercise the HIPPI interface using two systems, it writes data from the remote system to the local system. It then compares the actual data read with expected values.

If one of these tests fails, the failing hardware may be the HIPPI network interface that is tested or the midplane. The failing FRU may be the HIPPI-S board that contains the failing network interface or the midplane.

3.14.1 Prerequisites for Running the Vicious HIPPI Tests

These tests have the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must specify values for all parameters in the Test Options list that are set to `NEEDED` by default.
- You must use the HIPPI device down with the `ifconfig` command (for example, `ifconfig hip0 down`).
- To run the VHT_LOOPBACK test, you must connect a loopback cable on the HIPPI-S board that you want to test.
- To run the VHT_REMOTE test on one system, you must connect a loopback cable on the HIPPI-S board that you want to test.
- To run the VHT_REMOTE test on two systems, you must have access to a functional remote system that is running a version of IRIX that is equal to or newer than the version on the on the local system. (For example, if the local system is running IRIX version 6.2, then the remote system should be running IRIX version 6.2, 6.4, or 6.5. If the local system is running IRIX version 6.5, then the remote system must be running IRIX version 6.5.)

3.14.2 Running the Vicious HIPPI Tests

You can run the vicious HIPPI tests from the `field_diag` program.

3.14.2.1 Running the VHT_LOOPBACK Test

To run the VHT_LOOPBACK test from the `field_diag` program, perform the following procedure:

1. Select the VHT_LOOPBACK option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-9 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-9 VHT_LOOPBACK Test User-Adjustable Parameters

Parameter	Description
<code>num. packets</code>	Specifies the number of data packets that the test will send.
<code>Pattern</code>	Specifies the data pattern that the test will use. Enter a hexadecimal number or any of the following settings: <code>bits</code> specifies that each 64-bit data word will have a random sequence of consecutive 1 bits. <code>slide0</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 1; bit 0 will be set to 0. Subsequent data words will be circularly left-shifted by one bit position. <code>slide1</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 0; bit 0 will be set to 1. Subsequent data words will be circularly left-shifted by one bit position. <code>random</code> specifies that data words will be set to random values. <code>all</code> specifies that the test will use built-in data patterns, one per pass, in a circular pattern.

Table 3-9 (continued) VHT_LOOPBACK Test User-Adjustable Parameters

Parameter	Description
Block size	Specifies the block size to use for I/O operations (in bytes). Enter <code>pass</code> or one or more values in the following format: <i>max</i> [: <i>min</i> [: <i>step</i>]] When you use the <i>min</i> value, the test randomly generates a block size value in the range from <i>min</i> to <i>max</i> . If you specify a <i>step</i> value, the randomly selected value increases by the value of <i>step</i> for each pass of the test, until the test reaches the <i>max</i> value. When you use the <code>pass</code> option, the test sets the block size to the current pass count value.
Interface	Specifies the device name of the HIPPI interface that the test will use. (For example, <code>/dev/hippi1</code>)

3. Press the left arrow key to return to the VHT_LOOPBACK option in the Diagnostics list.
4. Press the **Enter** key to start running the VHT_LOOPBACK test.
The message `Executing : VHT_LOOPBACK` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the VHT_LOOPBACK test.

3.14.2.2 Running the VHT_REMOTE Test

To run the VHT_REMOTE test from the `field_diag` program, perform the following procedure:

1. Select the VHT_REMOTE option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-10 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-10 VHT_REMOTE Test User-Adjustable Parameters

Parameter	Description
num. packets	Specifies the number of data packets that the test will send.
remote host	Specifies the remote system with which the test will communicate. Enter a hostname or IP address. You must set this parameter.
Pattern	Specifies the data pattern that the test will use. Specify a hexadecimal number or any of the following settings: <i>bits</i> specifies that each 64-bit data word will have a random sequence of consecutive 1 bits. <i>slide0</i> specifies that the first 64-bit data word will have all bits (except bit 0) set to 1; bit 0 will be set to 0. Subsequent data words will be circularly left-shifted by one bit position. <i>slide1</i> specifies that the first 64-bit data word will have all bits (except bit 0) set to 0; bit 0 will be set to 1. Subsequent data words will be circularly left-shifted by one bit position. <i>random</i> specifies that data words will be set to random values. <i>all</i> specifies that the test should use built-in data patterns, one per pass, in a circular pattern.
Block size	Specifies the block size to use for I/O operations (in bytes). Specify <i>pass</i> or one or more values in the following format: <i>max</i> [<i>min</i> [: <i>step</i>]] When you use the <i>min</i> value, the test randomly generates a block size value in the range from <i>min</i> to <i>max</i> . If you specify a <i>step</i> value, the randomly selected value increases by the value of <i>step</i> for each pass of the test, until the test reaches the <i>max</i> value. When you use the <i>pass</i> option, the test sets the block size to the current pass count value.
I-field	Specifies the I-field value for the HIPPI interface on the local host. See the <i>/usr/etc/hippi</i> file or use the <i>hipmap</i> command to determine the I-field value. (For example, 0x0300001d, 0x01000004, and 0x3c) You must set this parameter. If you are using one system in loopback mode or two systems that are directly connected (that is, no HIPPI switch is used), the value of this parameter is not important. Set this parameter to 0x00000000 in either of these situations.
Local Interface	Specifies the device name of the HIPPI interface on the local system. (This device will check for writes. For example, /dev/hippi0) You must set this parameter.
Remote Interface	Specifies the device name of the HIPPI interface on the remote system. (This device will send the writes. For example, /dev/hippi1) To run the test in loopback mode, set the <i>Local Interface</i> and <i>Remote Interface</i> parameters to the same value. You must set this parameter.

Table 3-10 (continued) VHT_REMOTE Test User-Adjustable Parameters

Parameter	Description
Login Name	Specifies the login (username) of the account to use on the remote host. (The default is root.)
Password	Specifies the password for the account on the remote host (if required).

3. Press the left arrow key to return to the VHT_REMOTE option in the Diagnostics list.
4. Press the **Enter** key to start running the VHT_REMOTE test.
The message `Executing : VHT_REMOTE` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the VHT_REMOTE test.

3.14.3 Output from the Vicious HIPPI Tests

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.14.3.1 Pass Output

If the VHT_LOOPBACK test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `VHT_LOOPBACK Test Passed`.

If the VHT_REMOTE test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `VHT_REMOTE Test Passed`.

3.14.3.2 Failure Output

If the VHT_LOOPBACK test detects a hardware failure or encounters an error that makes it unable to continue testing, the diagnostic status area of the user interface displays the message `VHT_LOOPBACK Test Failed`.

If the VHT_REMOTE test detects a hardware failure or encounters an error that makes it unable to continue testing, the diagnostic status area of the user interface displays the message `VHT_REMOTE Test Failed`.

If one of these messages appears, view the Test Log Output window for more information about the cause of the failure. Possible messages include:

- ***** Data Compare Error *****
Differences written to "file".
Current pass = 1 Current data pattern = slide1

The test failed because the actual data did not equal the expected data. The failing FRU may be the HIPPI-S board. Refer to the generated error file for more information.

The following output shows a partial sample of the contents of an error file:

```
Pass : 1
A : expected
B : actual
x : logical difference

A( 0)+ 8000000000000198 0000000100800080 *.....*
B( 0)+ 8000000000000198 0000000000000000 *.....*
x( 0)+ 0000000000000000 0000000100800080 *.....*
A(10)+ 00000000007f0000 0000000000000006 *.....*
B(10)+ 0000000000000001 0000000000000002 *.....*
x(10)+ 00000000007f0001 0000000000000004 *.....*
A(20)+ 0000000001ffe00 0000ffffffffffe00 *.....*
B(20)+ 0000000000000004 0000000000000008 *.....*

x(20)+ 0000000001ffe04 0000ffffffffffe08 *.....*
```

- Executing Write Test
connect() failed, errno = 152, text = "Connection refused".

A write operation failed because there was no remote system to receive the data. This failure may have occurred because the remote system was not running or because the network interface failed. The failing FRU may be the HIPPI-S board.

3.15 Vicious Socket Tests

The vicious socket tests are socket-based exercisers that use TCP sockets to send and receive data across a network. There are two vicious socket tests:

- The VST_PING test uses ICMP to send data packets to a remote system, which then returns the packets to the vicious socket test process on the local system. The local system verifies that the data returned from the remote system is the same as the data sent to the remote system. You must run this test as root.
- The VST_RW test reads and writes data patterns between two systems. It compares the data that is sent from the remote system with the data that is received by the local system.

If one of these tests fails, the failing hardware is the network interface that is tested or the midplane. The failing FRU is the board that contains the failing network interface or the midplane.

3.15.1 Prerequisites for Running the Vicious Socket Tests

These tests have the following prerequisites:

- The system that you want to test must have the IRIX operating system booted and running.
- You must have access to a remote system that is functional and is running the IRIX operating system if you want to run the vicious socket test between two systems. (The version of IRIX on the remote system must be equal to or newer than the version on the local system. [For example, if the local system is running IRIX version 6.2, then the remote system must be running IRIX version 6.2, 6.4, or 6.5. If the local system is running IRIX version 6.5, then the remote system must be running IRIX version 6.5.])
- You must specify values for all parameters in the Test Options list that are set to `NEEDED` by default.

3.15.2 Running the Vicious Socket Tests

You can run the vicious socket tests from the `field_diag` program.

3.15.2.1 Running the VST_PING Test

To run the VST_PING test from the field_diag program, perform the following procedure:

1. Select the VST_PING option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions for each parameter; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-11 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-11 VST_PING Test User-Adjustable Parameters

Parameter	Description
num. packets	Specifies the number of data packets that the test will send to the remote system.
remote host	Specifies the host to which the test will send the data packets. You must enter this parameter.
Pattern	Specifies the data pattern that the test will use. Enter a hexadecimal number or any of the following settings: <code>bits</code> specifies that each 64-bit data word will have a random sequence of consecutive 1 bits. <code>slide0</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 1; bit 0 will be set to 0. Subsequent data words will be circularly left-shifted by one bit position. <code>slide1</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 0; bit 0 will be set to 1. Subsequent data words will be circularly left-shifted by one bit position. <code>random</code> specifies that data words will be set to random values. <code>all</code> specifies that the test will use built-in data patterns, one per pass, in a circular pattern.

3. Press the left arrow key to return to the VST_PING option in the Diagnostics list.
4. Press the **Enter** key to start running the VST_PING test.

The message `Executing : VST_PING` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the VST_PING test.

3.15.2.2 Running the VST_RW Test

To run the VST_RW test from the field_diag program, perform the following procedure:

1. Select the VST_RW option from the Diagnostics list.
2. If you want to modify any of the parameters, perform the following actions for each parameter; otherwise, go to Step 4.
 - Press the right arrow key to move into the Diagnostic Options list.
 - Select the parameter that you want to modify. (Table 3-12 describes the parameters that you can modify.)
 - Press the **Enter** key.
 - At the prompt `Enter New Value for the Option` in the diagnostic status area, enter the new value.
 - Press the **Enter** key.

Table 3-12 VST_RW Test User-Adjustable Parameters

Parameter	Description
num. packets	Specifies the number of data packets that the test will send to the remote system.
remote host	Specifies the host to which the test will send the data packets. You must enter this parameter.
Pattern	Specifies the data pattern that the test will use. Enter a hexadecimal number or any of the following settings: <code>bits</code> specifies that each 64-bit data word will have a random sequence of consecutive 1 bits. <code>slide0</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 1; bit 0 will be set to 0. Subsequent data words will be circularly left-shifted by one bit position. <code>slide1</code> specifies that the first 64-bit data word will have all bits (except bit 0) set to 0; bit 0 will be set to 1. Subsequent data words will be circularly left-shifted by one bit position. <code>random</code> specifies that data words will be set to random values. <code>all</code> specifies that the test will use built-in data patterns, one per pass, in a circular pattern.
Block size	Specifies the block size to use for I/O operations. Specify a number of bytes or <code>pass</code> . <code>pass</code> indicates that the test will set the blocksize to the current pass count value.
Login Name	Specifies the login (username) of the account to use on the remote host. (The default is root.)
Password	Specifies the password for the account on the remote host (if required).

3. Press the left arrow key to return to the VST_RW option in the Diagnostics list.
4. Press the **Enter** key to start running the VST_RW test.
The message `Executing : VST_RW` appears in the diagnostic status area, which indicates that the test is running.
5. View the diagnostic status area and Test Log Output window for messages from the VST_RW test.

3.15.3 Output from the Vicious Socket Tests

The tests return output to the diagnostic status area of the user interface and the Test Log Output window.

3.15.3.1 Pass Output

If the VST_PING test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `VST_PING Test Passed`.

If the VST_RW test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `VST_RW Test Passed`.

3.15.3.2 Failure Output

If the VST_PING test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `VST_PING Test Failed`.

If the VST_RW test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `VST_RW Test Failed`.

If one of these messages appears, view the Test Log Output window for more information about the cause of the failure. Possible messages include:

- Executing Ping Test
Ping timed out
Expected to write *num* bytes, sent 0 instead
***** Error *****
Current pass = 1
Current data pattern = *pattern*

The VST_PING test failed because the remote system did not respond. The failing FRU may be the board that contains the network interface that connects to the remote system.

- Executing Write Test
connect() failed, errno = 152, text = "Connection refused".

A write operation failed because there was no remote system to receive the data. This failure may have occurred because the remote system was not running or because the network interface failed. The failing FRU may be the HIPPI-S board.

- ***** Data Compare Error *****
Differences written to "*file*".
Current pass = 1
Current data pattern = slide0

The test failed because the actual data did not equal the expected data. The failing FRU may be the board that contains the network interface that is being tested. Refer to the generated error file for more information.

The following output shows a partial sample of the contents of an error file:

Executing Read Test

A : expected
B : actual
x : logical difference

```
A ( 0) + fe fd fb f7 ef df bf 7f fe fd fb f7 ef df bf 7f *.....*
B ( 0) + 7f 06 3c 38 7c fc 1f 06 06 0c 7f ff ff ff 1c 3e *..<8|.....>*
x ( 0) + 81 fb c7 cf 93 23 a0 79 f8 f1 84 08 10 20 a3 41 *.....#.y.....A*
A (10) + fe fd fb f7 ef df bf 7f fe fd fb f7 ef df bf 7f *.....*
B (10) + 70 3c 0e c0 1f 3f ff fc 07 18 f8 70 fe c0 ff ff *p<...?.p...*
x (10) + 8e c1 f5 37 f0 e0 40 83 f9 e5 03 87 11 1f 40 80 *...7..@.....@.*
A (20) + fe fd fb f7 ef df bf 7f fe fd fb f7 ef df bf 7f *.....*
B (20) + fe 20 7e 7f 07 7f 3e 3f ff 30 08 ff 20 20 03 78 *..~...>?.0.....x*
x (20) + 00 dd 85 88 e8 a0 81 40 01 cd f3 08 cf ff bc 07 *.....@.....*
A (30) + fe fd *.....*
B (30) + 38 02 *8.....*
x (30) + c6 ff *.....*
```

3.16 RAID Verification Test

The RAID verification test creates an inventory of all customer-replaceable units in an attached RAID array. This test does not write to or read from the RAID array. However, it does query the disks with pass-through commands, which checks the integrity of the SCSI bus to the disk drive level.

This test writes the configuration information into `/usr/diags/raid/configfile-x-y` files, where `x` specifies the fiber channel controller number and `y` specifies the RAID unit number. (The test creates multiple files for the RAID array.)

You should run this test when the RAID array is first installed (after initial power-on). After the test passes, run the System Verification Program (SVP) to send the configuration to the Mosaic database.

If this test fails, the failing FRU is a disk drive, RAID controller, or midplane.

3.16.1 Prerequisite for Running the RAID Verification Test

This test has the following prerequisite: The system that you want to test must have the IRIX operating system booted and running.

3.16.2 Running the RAID Verification Test

1. Select the RAID option from the Diagnostics list.
2. Press the **Enter** key to start running the RAID verification test.
The message `Executing : RAID` appears in the diagnostic status area, which indicates that the test is running.
3. View the diagnostic status area and Test Log Output window for messages from the RAID verification test.

Note: The System Verification Program (SVP) also runs this test. For more information about SVP, refer to the *System Verification Program (SVP) Reference Guide*, Silicon Graphics part number 108-0165-005.

3.16.3 Output from the RAID Verification Test

This test returns output to the diagnostic status area of the user interface and the Test Log Output window.

3.16.3.1 Pass Output

If this test completes without detecting any hardware failures, the diagnostic status area of the user interface displays the message `RAID Passed`.

3.16.3.2 Failure Output

If this test detects a hardware failure or encounters an error that makes the test unable to continue, the diagnostic status area of the user interface displays the message `RAID Failed`.

If this message appears, view the Test Log Output window for more information about the cause of the failure.

3.17 Summary of Chapter 3

This chapter described the diagnostic tests that you can run from the field_diag user interface. Table 3-13 summarizes the pass and fail messages and the failing FRU for each test.

Table 3-13 Summary of Diagnostic Test Information

Diagnostic Test	Pass Message	Fail Message	Failing FRU
ATM LINC DMA verification test	ATM Test Passed	ATM Test Failed	Node board, midplane, or ATM board
CPU Instruction test	CPU-INSTRUCTION Test Passed	CPU-INSTRUCTION Test Failed	Node board or midplane
DIVO hardware test	DIVO Test Passed	DIVO Test Failed	DIVO board or midplane
Floating-point unit (single-precision) test	Floating point single precision Test Passed	Floating point single precision Test Failed	Node board or midplane
Floating-point unit (double-precision) test	Floating point double precision Test Passed	Floating point double precision test Failed	Node board or midplane
HIPPI LINC DMA verification test	HIPPI Test Passed	HIPPI Test Failed	Node board, HIPPI-S board, or midplane
InfiniteReality graphics test	IRSAUDIT Passed	IRSAUDIT Failed	Graphics subsystem (RM, GE, or DG) or midplane
MediaIO board test	MIO Test Passed	MIO Test Failed	MediaIO board or midplane
Memory test	memory Test Passed	memory Test Failed	DIMM, Node board, or midplane
Network thrasher test	network Test Passed	network Test Failed	ATM, BaseIO, HIPPI-S, or MENET board or midplane
RAID verification test	RAID Test Passed	RAID Test Failed	Disk drive, RAID controller, or midplane
Router tests	ROUTER-CONFIDENCE Test Passed or ROUTER-RANDOM Test Passed or ROUTER-SPECIFIC Test Passed	ROUTER-CONFIDENCE Test Failed or ROUTER-RANDOM Test Failed or ROUTER-SPECIFIC Test Failed	Node board, Router board, MetaRouter board, CrayLink cable, or midplane

Table 3-13 (continued) Summary of Diagnostic Test Information

Diagnostic Test	Pass Message	Fail Message	Failing FRU
SCSI thrasher test	randomthrash Test Passed	randomthrash Test Failed	BaseIO board, MSCSI board, disk drive, or midplane
Vicious HIPPI tests	VHT_LOOPBACK Test Passed or VHT_REMOTE Test Passed	VHT_LOOPBACK Test Failed or VHT_REMOTE Test Failed	HIPPI-S board or midplane
Vicious socket tests	VST_PING Test Passed or VST_RW Test Passed	VST_PING Test Failed or VST_RW Test Failed	BaseIO board, MENET board, ATM board, HIPPI-S board, or midplane